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				Application Number	August 20, 2003	
			LOSURE	Filing Date		
			LICANT	First Named Inventor	Viktor Koldiaev	
				Group Art Unit		
(use as may sheets as necessary)			essary)	Examiner Name		
Sheet	1	of	1	Attorney Docket Number	88103.0001	

	OTHER PRIOR ART - NON PATENT LITERATURE DOCUMENTS						
Examiner Initials*	Cite No. ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T²				
W		Chin-hsing Yu, et al., Ultrathin (2.7nm) Oxy-nitride for Suppressing Boron Penetration Characterized by Direct Hole Tunneling Current in p+/pMOS, Technology Development Center, Worldwide Semiconductor Manufacturing Corp., Hsinchu, Taiwan 30077, Republic of China					
F		S.C. Song, et al., Ultra Thin high quality stack nitride/oxide gate dielectrics prepared by in-situ rapid thermal N ₂ 0 oxidation of NH ₃ -nitrided Si, Mecroelectronics Research Center, Department of Electrical and Computer Engineering The University of Texas, Austin, TX					
W.		Quazi D. M., Khosru, et al., Low Thermal-Budget Ultrathin NH ₃ -Annealed Atomic-Layer-Deposited Si-Nitride/Si0 ₂ Stack Gate Dielectrics With Excellent Reliability, IEEE Electron Device Letters, Vol. 23, No. 4, April 2002					
A CONTRACTOR OF THE PARTY OF TH		Quazi D.M., Khosru, et al., Ultrathin NH ₃ Annealed Atomic Layer Deposited Si-nitride/Si02 Stack Gate Dielectrics with High Reliability, Research Centr for Nanodevices and Systems, Hiroshima University, 1-4-2 Kagamiyama, Higashi-Hiroshima, Japan					

Examiner Signature V. MONDT	Date Considered 10/14/04	
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